

Frontiers
in Electronic
Testing

Advances in Electronic Testing

Challenges and Methodologies

Edited by
Dimitris Glezopoulos



Springer



Advances In Electronic Testing Challenges And Methodologies Frontiers In Electronic Testing

Mourad Fakhfakh, Esteban Tlelo-Cuautle, Patrick Siarry



Advances In Electronic Testing Challenges And Methodologies Frontiers In Electronic Testing:

Advances in Electronic Testing Dimitris Gizopoulos, 2006-01-22 *Advances in Electronic Testing Challenges and Methodologies* is a new type of edited volume in the *Frontiers in Electronic Testing* book series devoted to recent advances in electronic circuits testing. The book is a comprehensive elaboration on important topics which capture major research and development efforts today. The motivation and inspiration behind this book is to deliver a thorough text that focuses on the evolution of test technology, provides insight about the abiding importance of discussed topics, records today's state of the art and industrial practices and trends, reveals the challenges for emerging testing methodologies and envisages the future of this journey. The book consists of eleven edited chapters written by experts in: Defect Oriented Testing, Nanometer Technologies, Failures and Testing, Silicon Debug, Delay Testing, High Speed Test Interfaces, DFT Oriented, Low Cost Testers, Embedded Cores and System on Chip Testing, Memory Testing, Mixed Signal Testing, RF Testing and Loaded Board Testing. Contributing authors are affiliated with, in alphabetical order: Agilent, ARM, Balearic Islands Univ, IBM, Inovys, Intel, LogicVision, Magma, Mentor Graphics, New Mexico Univ, Sandia National Labs, Synopsys, Teradyne and Texas Instruments. *Advances in Electronic Testing Challenges and Methodologies* is an advanced textbook and reference point for senior undergraduate and graduate students in MSc or PhD tracks, professors and research leaders in the electronic testing domain. It is also for industry design and test engineers and managers seeking a global view and understanding of test technology practices and methodologies and a dense elaboration on test related issues they face in their development projects. There is a definite need for documenting the advances in testing. I find the work of this edited volume by Dimitris Gizopoulos and his team of authors to be significant and timely; the book provides besides novel test methodologies a collective insight into the emerging aspects of testing. This I think is beneficial to practicing engineers and researchers both of whom must stay at the forefront of technology. This latest addition to the *Frontiers Series* is destined to serve an important role. From the Foreword by Vishwani D Agrawal, Consulting Editor, *Frontiers in Electronic Testing Book Series*. *System-on-Chip Test Architectures* Laung-Terng Wang, Charles E. Stroud, Nur A. Touba, 2010-07-28 Modern electronics testing has a legacy of more than 40 years. The introduction of new technologies, especially nanometer technologies with 90nm or smaller geometry, has allowed the semiconductor industry to keep pace with the increased performance capacity demands from consumers. As a result, semiconductor test costs have been growing steadily and typically amount to 40% of today's overall product cost. This book is a comprehensive guide to new VLSI Testing and Design for Testability techniques that will allow students, researchers, DFT practitioners and VLSI designers to master quickly System on Chip Test architectures for test debug and diagnosis of digital memory and analog mixed signal designs. Emphasizes VLSI Test principles and Design for Testability architectures with numerous illustrations/examples. Most up to date coverage available including Fault Tolerance, Low Power Testing, Defect and Error Tolerance, Network on Chip, NOC Testing, Software Based Self Testing, FPGA Testing, MEMS Testing and System In

Package SIP Testing which are not yet available in any testing book Covers the entire spectrum of VLSI testing and DFT architectures from digital and analog to memory circuits and fault diagnosis and self repair from digital to memory circuits Discusses future nanotechnology test trends and challenges facing the nanometer design era promising nanotechnology test techniques including Quantum Dots Cellular Automata Carbon Nanotubes and Hybrid Semiconductor Nanowire Molecular Computing Practical problems at the end of each chapter for students *Machine Learning Support for Fault Diagnosis of System-on-Chip* Patrick Girard, Shawn Blanton, Li-C. Wang, 2023-03-13 This book provides a state of the art guide to Machine Learning ML based techniques that have been shown to be highly efficient for diagnosis of failures in electronic circuits and systems The methods discussed can be used for volume diagnosis after manufacturing or for diagnosis of customer returns Readers will be enabled to deal with huge amount of insightful test data that cannot be exploited otherwise in an efficient timely manner After some background on fault diagnosis and machine learning the authors explain and apply optimized techniques from the ML domain to solve the fault diagnosis problem in the realm of electronic system design and manufacturing These techniques can be used for failure isolation in logic or analog circuits board level fault diagnosis or even wafer level failure cluster identification Evaluation metrics as well as industrial case studies are used to emphasize the usefulness and benefits of using ML based diagnosis techniques **Emerging Nanotechnologies** Mohammad

Tehranipoor, 2007-12-08 *Emerging Nanotechnologies Test Defect Tolerance and Reliability* covers various technologies that have been developing over the last decades such as chemically assembled electronic nanotechnology Quantum dot Cellular Automata QCA and nanowires and carbon nanotubes Each of these technologies offers various advantages and disadvantages Some suffer from high power some work in very low temperatures and some others need indeterministic bottom up assembly These emerging technologies are not considered as a direct replacement for CMOS technology and may require a completely new architecture to achieve their functionality *Emerging Nanotechnologies Test Defect Tolerance and Reliability* brings all of these issues together in one place for readers and researchers who are interested in this rapidly changing field

Metrology and Diagnostic Techniques for Nanoelectronics Zhiyong Ma, David G. Seiler, 2017-03-27 Nanoelectronics is changing the way the world communicates and is transforming our daily lives Continuing Moore's law and miniaturization of low power semiconductor chips with ever increasing functionality have been relentlessly driving R D of new devices materials and process capabilities to meet performance power and cost requirements This book covers up to date advances in research and industry practices in nanometrology critical for continuing technology scaling and product innovation It holistically approaches the subject matter and addresses emerging and important topics in semiconductor R D and manufacturing It is a complete guide for metrology and diagnostic techniques essential for process technology electronics packaging and product development and debugging a unique approach compared to other books The authors are from academia government labs and industry and have vast experience and expertise in the topics presented The book is intended for all those involved in IC

manufacturing and nanoelectronics and for those studying nanoelectronics process and assembly technologies or working in device testing characterization and diagnostic techniques

Thermal Issues in Testing of Advanced Systems on Chip Nima Aghaee Ghaleshahi, 2015-09-23 Many cutting edge computer and electronic products are powered by advanced Systems on Chip SoC Advanced SoCs encompass superb performance together with large number of functions This is achieved by efficient integration of huge number of transistors Such very large scale integration is enabled by a core based design paradigm as well as deep submicron and 3D stacked IC technologies These technologies are susceptible to reliability and testing complications caused by thermal issues Three crucial thermal issues related to temperature variations temperature gradients and temperature cycling are addressed in this thesis Existing test scheduling techniques rely on temperature simulations to generate schedules that meet thermal constraints such as overheating prevention The difference between the simulated temperatures and the actual temperatures is called temperature error This error for past technologies is negligible However advanced SoCs experience large errors due to large process variations Such large errors have costly consequences such as overheating and must be taken care of This thesis presents an adaptive approach to generate test schedules that handle such temperature errors Advanced SoCs manufactured as 3D stacked ICs experience large temperature gradients Temperature gradients accelerate certain early life defect mechanisms These mechanisms can be artificially accelerated using gradient based burn in like operations so that the defects are detected before shipping Moreover temperature gradients exacerbate some delay related defects In order to detect such defects testing must be performed when appropriate temperature gradients are enforced A schedule based technique that enforces the temperature gradients for burn in like operations is proposed in this thesis This technique is further developed to support testing for delay related defects while appropriate gradients are enforced The last thermal issue addressed by this thesis is related to temperature cycling Temperature cycling test procedures are usually applied to safety critical applications to detect cycling related early life failures Such failures affect advanced SoCs particularly through silicon via structures in 3D stacked ICs An efficient schedule based cycling test technique that combines cycling acceleration with testing is proposed in this thesis The proposed technique fits into existing 3D testing procedures and does not require temperature chambers Therefore the overall cycling acceleration and testing cost can be drastically reduced All the proposed techniques have been implemented and evaluated with extensive experiments based on ITC 02 benchmarks as well as a number of 3D stacked ICs Experiments show that the proposed techniques work effectively and reduce the costs in particular the costs related to addressing thermal issues and early life failures We have also developed a fast temperature simulation technique based on a closed form solution for the temperature equations Experiments demonstrate that the proposed simulation technique reduces the schedule generation time by more than half

Multi-Chip Module Test Strategies Yervant Zorian, 2012-12-06 MCMs today consist of complex and dense VLSI devices mounted into packages that allow little physical access to internal nodes The complexity and cost

associated with their test and diagnosis are major obstacles to their use Multi Chip Module Test Strategies presents state of the art test strategies for MCMs This volume of original research is designed for engineers interested in practical implementations of MCM test solutions and for designers looking for leading edge test and design for testability solutions for their next designs Multi Chip Module Test Strategies consists of eight contributions by leading researchers It is designed to provide a comprehensive and well balanced coverage of the MCM test domain Multi Chip Module Test Strategies has also been published as a special issue of the Journal of Electronic Testing Theory and Applications JETTA Volume 10 Numbers 1 and 2 **Progress in VLSI Design and Test** Hafizur Rahaman, Sanatan Chattopadhyay, Santanu

Chattopadhyay, 2012-06-26 This book constitutes the refereed proceedings of the 16th International Symposium on VLSI Design and Test VDAT 2012 held in Shibpur India in July 2012 The 30 revised regular papers presented together with 10 short papers and 13 poster sessions were carefully selected from 135 submissions The papers are organized in topical sections on VLSI design design and modeling of digital circuits and systems testing and verification design for testability testing memories and regular logic arrays embedded systems hardware software co design and verification emerging technology nanoscale computing and nanotechnology **The Computer Engineering Handbook** Vojin G.

Oklobdzija, 2001-12-26 There is arguably no field in greater need of a comprehensive handbook than computer engineering The unparalleled rate of technological advancement the explosion of computer applications and the now in progress migration to a wireless world have made it difficult for engineers to keep up with all the developments in specialties outside their own References published only a few years ago are now sorely out of date The Computer Engineering Handbook changes all of that Under the leadership of Vojin Oklobdzija and a stellar editorial board some of the industry's foremost experts have joined forces to create what promises to be the definitive resource for computer design and engineering Instead of focusing on basic introductory material it forms a comprehensive state of the art review of the field's most recent achievements outstanding issues and future directions The world of computer engineering is vast and evolving so rapidly that what is cutting edge today may be obsolete in a few months While exploring the new developments trends and future directions of the field The Computer Engineering Handbook captures what is fundamental and of lasting value

Proceedings of the 2nd International Conference on the Frontiers of Robotics and Software Engineering (FRSE 2024) Jiangping Hu, Jin Zhang, 2025-03-07 The proceeding of FRSE presents a collection of innovation research in the cutting edge fields of robotics and software engineering It is highlighted within that there are novel methodologies critical analyses and breakthrough results which emphasize the enhanced or amplified results achieved when robotics technologies are integrated with advanced software This book is outfitted with numerous diagrams tables and conceptual frameworks structured to enhance comprehension and accessibility that facilitate a deeper understanding of complex topics The presentation is not just theoretical but includes case studies and real world applications offering a practical approach to

complex problem solving techniques across related industries Readers will receive benefits from this comprehensive resource gain a renewed understanding of contemporary challenges and innovative solutions in robotics and software engineering And this book will be a guide and asset for research scholars and professionals in robotics and software engineering looking to apply these cutting edge technologies in impactful ways *SMART Integrated Circuit Design and Methodology* Thomas Noulis, Costas Psychalinos, Alkis Hatzopoulos, 2023-12-07 This book describes advanced flows and methodologies for the design and implementation of system on chip SoC It is written by a mixture of industrial experts and key academic professors and researchers The intended audience is not only students but also engineers with system on chip and semiconductor background currently working in the semiconductor industry Integrated Circuits are available in every electronic product especially in emerging market segments such as 5G mobile communications autonomous driving fully electrified vehicles and artificial intelligence These product types require real time processing at billions of operations per second The development design cycle time is driving costs and time to market more than ever before The traditional design methodologies have reached their limits and innovative solutions are essential to serve the emerging SoC design challenges In the framework of the Circuit and System Society CASS Outreach Initiative 2022 call the SMART Integrated Circuits design methodology named SMARTIC Seasonal School was performed in November 2022 in Thessaloniki Greece Features Core analog circuits of any system on chip such as high performance rectifiers and filters are addressed in detail together with their respective design methodology New advanced methodologies towards design cycle speed up based on machine learning and artificial intelligence applications Advanced analog design methodology based on gm Id and look up tables A powerful flow for enabling fast time to market analog circuit design focusing on baseband circuits More exotic methodologies and applications with focus on digital based analog processing in nanoscale CMOS ICs and the design and development of depleted monolithic active pixel sensors for high radiation applications together with all the respective challenges of this application Advances in inhalation safety assessment Victoria Hutter, Ewelina Hoffman, Thomas Hofmann, Mary Catherine McElroy, 2025-09-16 Inhalation toxicology refers to the route of exposure whereby an inhaled substance can cause direct local toxicity to the airways or exert their action systemically following absorption and metabolism Assessing the inhalation safety of substances is applicable to many sectors producing new products with intended or inadvertent inhaled exposure e g pharmaceutical chemical environmental consumer products Limitations with current tools and approaches to assessing inhalation safety prevent a high proportion of these products ever making it to market due to preclinical safety concerns in animals despite not understanding the relevance of these responses in humans Assessing the safety of substances in the lungs requires in depth knowledge of the properties of the inhaled aerosol deposition within the lungs interaction with biological barriers and the innate immune system and translation of these events to human safety As such advancements in inhalation toxicology rely on interdisciplinary research between the interfaces of the biological and physical sciences

Assessing the inhalation safety of substances applicable to many areas which traditionally have used different approaches e.g pharmaceutical chemical environmental consumer products However the challenges of providing relevant dosing replicating exposure categorizing adaptive and adverse responses and applicability of human relevant inhalation risk assessment remain a challenge to all sectors This research topic aims to explore new approaches and technologies within inhalation toxicity and other related fundamental research including aerosol characterisation which have the potential to answer the unmet needs within safety assessment

Advances in Computer Vision: From Deep Learning Models to Practical Applications Hancheng Zhu,Lu Tang,Rui Yao,Yanqiu Huang,2025-05-26 The field of computer vision has experienced remarkable progress in recent years largely attributed to the unprecedented advancements in deep learning models and their practical applications across diverse domains This research topic is dedicated to presenting and exploring the latest developments in computer vision with a particular emphasis on the transition from theoretical deep learning models to their real world applications This research topic focuses on the practical application of deep learning models in computer vision translating theoretical advancements into real world solutions It offers a platform to share success stories and case studies illustrating the effective deployment of such models in areas like medical imaging remote sensing and multimedia affective computing Furthermore with the importance of interpretability and transparency in deep learning models emphasized these models become more complex and understanding their decision making processes is crucial

Advanced Digital Technologies in Digitalized Smart Grid Xiangjun Zeng,Yan Xu,Dongqi Liu,2022-11-08 Thin Film Deposition Techniques Tahir Iqbal Awan,Sumera Afsheen,Sabah Kausar,2025-01-02 This book describes the basics and historical aspects of thin film The introductory chapter of this book contains various aspects about thin film deposition methods significance of nanomaterials in the fabrication of thin film certain fundamental characteristics of thin films electrical optical and morphological some challenges thickness uniformity film adhesion issues temperature related challenges film defects and quality control preparation of the surface of the substrate before deposition etc faced during the formation of thin film significance and different types of deposition techniques along with their basic introduction working principle construction merits demerits and also application in specific fields This book specifically works on the techniques of thin film deposition and role of the thin film in the formation of these deposition methods

Computational Intelligence in Digital and Network Designs and Applications Mourad Fakhfakh,Esteban Tlelo-Cuautle,Patrick Siarry,2015-07-14 This book explains the application of recent advances in computational intelligence algorithms design methodologies and synthesis techniques to the design of integrated circuits and systems It highlights new biasing and sizing approaches and optimization techniques and their application to the design of high performance digital VLSI radio frequency and mixed signal circuits and systems This second of two related volumes addresses digital and network designs and applications with 12 chapters grouped into parts on digital circuit design network optimization and applications It will be of interest to practitioners and researchers in computer science and electronics

engineering engaged with the design of electronic circuits

Advanced Anomaly Detection Technologies and Applications in Energy Systems Tinghui Ouyang, Yusen He, Xun Shen, Zhenhao Tang, Yahui Zhang, 2025-02-17

Anomaly detection is an important topic which has been well studied in diverse research areas and application domains. It generally involves detection of abnormal data, unhealthy status, fault diagnosis, and can be helpful to guarantee industrial systems stability, security, and economy. As development of intelligent industries and sensor systems grows, large amounts of data become easily available, and challenges arise in industrial systems anomaly detection. One typical case is the study within energy-related systems like thermal energy, renewable energy, study, e.g., wind energy, photovoltaic, electric vehicles, and so on. These systems can involve various data formats and more complex data structures, making anomaly data detection a challenge. Currently, under the development of deep learning and big data analytics, many promising results have been achieved in energy systems anomaly data detection. However, many challenging problems remain unsolved due to the complex nature of energy industries. New techniques and advanced engineering applications on anomaly detection in energy systems still appeal to a wide range of scholars and industries.

Advances in Polymer Composite Research Atul Kumar Maurya, Gaurav Manik, Sushanta Kumar Sethi, 2025-05-06 This book emphasizes the importance of experimental characterization techniques and computational modeling tools in polymer composites. The topics covered include finite element analysis, computational fluid dynamics, molecular dynamics, simulations, machine learning, material informatics, multiscale modeling, advanced characterization techniques, and the emerging field of nanocomposites. Each chapter provides detailed discussions, case studies, and examples to illustrate the practical application of these techniques in polymer composite research. Features: Offers a comprehensive exploration of polymer composites encompassing both experimental and computational approaches. Showcases most recent findings, methodologies, technologies, and applications in the field. Explores real-world case studies, industrial applications, and potential commercialization opportunities. Discusses the understanding, analysis, and design of polymer composites. Includes LAMMPS, Ansys, ABAQUS, and Materials Studio based simulation examples. This book is aimed at graduate students and researchers in polymers, polymer composites, and materials science.

A Space Bibliography: Through 1958 Air University (U.S.). Aerospace Studies Institute. Documentary Research Division, Raymond Estep, 1959. Contents: Astronomy, Bibliography, Biography, and autobiography, Commands, installations, and organizations, Electronics, communications, control, and guidance, History, Manufacturing, materials, and methods, Missiles, rockets, and rocket-powered aircraft, Orbits and trajectories, Propulsion engines and propellants, Research and testing, Satellite vehicles, Space flight, Space law, Space medicine.

Agile Security in the Digital Era Mounia Zaydi, Youness Khourdif, Bouchaib Nassereddine, Justin Zhang, 2024-12-30 In an era defined by rapid digital transformation, Agile Security in the Digital Era: Challenges and Cybersecurity Trends emerges as a pivotal resource for navigating the complex and ever-evolving cybersecurity landscape. This book offers a comprehensive exploration of how agile methodologies

can be integrated into cybersecurity practices to address both current challenges and anticipate future threats Through a blend of theoretical insights and practical applications it equips professionals with the tools necessary to develop proactive security strategies that are robust flexible and effective The key features of the book below highlight these innovative approaches

- Integration of agile practices Detailed guidance on incorporating agile methodologies into cybersecurity frameworks to enhance adaptability and responsiveness
- Comprehensive case studies Real world applications and case studies that demonstrate the successful implementation of agile security strategies across various industries
- Future proof security tactics Insights into emerging technologies such as blockchain and IoT offering a forward looking perspective on how to harness these innovations securely

Intended for cybersecurity professionals IT managers and policymakers Agile Security in the Digital Era serves as an essential guide to understanding and implementing advanced security measures in a digital world The book provides actionable intelligence and strategies enabling readers to stay ahead of the curve in a landscape where agile responsiveness is just as crucial as defensive capability With its focus on cutting edge research and practical solutions this book is a valuable asset for anyone committed to securing digital assets against the increasing sophistication of cyber threats

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