

Cheol Seong Hwang *Editor*

Atomic Layer Deposition for Semiconductors

 Springer

Atomic Layer Deposition For Semiconductors

Karin Nielsen-Saines



Atomic Layer Deposition For Semiconductors:

Atomic Layer Deposition for Semiconductors Cheol Seong Hwang, 2013-10-18 Offering thorough coverage of atomic layer deposition ALD this book moves from basic chemistry of ALD and modeling of processes to examine ALD in memory logic devices and machines Reviews history operating principles and ALD processes for each device Amorphous Oxide Semiconductors Hideo Hosono, Hideya Kumomi, 2022-05-31 AMORPHOUS OXIDE SEMICONDUCTORS A singular resource on amorphous oxide semiconductors edited by a world recognized pioneer in the field In Amorphous Oxide Semiconductors IGZO and Related Materials for Display and Memory the Editors deliver a comprehensive account of the current status of and latest developments in transparent oxide semiconductor technology With contributions from leading international researchers and exponents in the field this edited volume covers physical fundamentals thin film transistor applications processing circuits and device simulation display and memory applications and new materials relevant to amorphous oxide semiconductors The book makes extensive use of structural diagrams of materials energy level and energy band diagrams device structure illustrations and graphs of device transfer characteristics photographs and micrographs to help illustrate the concepts discussed within It also includes A thorough introduction to amorphous oxide semiconductors including discussions of commercial demand common challenges faced during their manufacture and materials design Comprehensive explorations of the electronic structure of amorphous oxide semiconductors structural randomness doping limits and defects Practical discussions of amorphous oxide semiconductor processing including oxide materials and interfaces for application and solution process metal oxide semiconductors for flexible electronics In depth examinations of thin film transistors TFTs including the trade off relationship between mobility and reliability in oxide TFTs Perfect for practicing scientists engineers and device technologists working with transparent semiconductor systems Amorphous Oxide Semiconductors IGZO and Related Materials for Display and Memory will also earn a place in the libraries of students studying oxides and other non classical and innovative semiconductor devices WILEY SID Series in Display Technology Series Editor Ian Sage Abelian Services Malvern UK The Society for Information Display SID is an international society which has the aim of encouraging the development of all aspects of the field of information display Complementary to the aims of the society the Wiley SID series is intended to explain the latest developments in information display technology at a professional level The broad scope of the series addresses all facets of information displays from technical aspects through systems and prototypes to standards and ergonomics *Handbook of Semiconductor Manufacturing Technology* Yoshio Nishi, Robert Doering, 2017-12-19 Retaining the comprehensive and in depth approach that cemented the bestselling first edition s place as a standard reference in the field the Handbook of Semiconductor Manufacturing Technology Second Edition features new and updated material that keeps it at the vanguard of today s most dynamic and rapidly growing field Iconic experts Robert Doering and Yoshio Nishi have again assembled a team of the world s leading specialists in every area of semiconductor

manufacturing to provide the most reliable authoritative and industry leading information available Stay Current with the Latest Technologies In addition to updates to nearly every existing chapter this edition features five entirely new contributions on Silicon on insulator SOI materials and devices Supercritical CO₂ in semiconductor cleaning Low dielectrics Atomic layer deposition Damascene copper electroplating Effects of terrestrial radiation on integrated circuits ICs Reflecting rapid progress in many areas several chapters were heavily revised and updated and in some cases rewritten to reflect rapid advances in such areas as interconnect technologies gate dielectrics photomask fabrication IC packaging and 300 mm wafer fabrication While no book can be up to the minute with the advances in the semiconductor field the Handbook of Semiconductor Manufacturing Technology keeps the most important data methods tools and techniques close at hand

Advanced Gate Stacks for High-Mobility Semiconductors Athanasios Dimoulas, Evgeni Gusev, Paul C. McIntyre, Marc Heyns, 2008-01-01 This book provides a comprehensive monograph on gate stacks in semiconductor technology It covers the major latest developments and basics and will be useful as a reference work for researchers engineers and graduate students alike The reader will get a clear view of what has been done so far what is the state of the art and which are the main challenges ahead before we come any closer to a viable Ge and III V MOS technology *Semiconductors* Rosalinda

Inguanta, Carmelo Sunseri, 2018-03-07 Semiconducting materials are widely used in several applications such as photonics photovoltaics electronics and thermoelectrics because of their optical and electro optical features The fundamental and technological importance of these materials is due to the unique physical and chemical properties Over the years numerous methods have been developed for the synthesis of high efficient semiconductors Moreover a variety of approach and characterization methods have been used to study the numerous and fascinating properties of the semiconducting materials This book collects new developments about semiconductors from the fundamental issues to their synthesis and applications Special attention has been devoted to electrochemical growth and characterization **Physics of Semiconductors** Aditya

Saxena, 2025-02-20 Physics of Semiconductors Core Principles is a comprehensive guide that demystifies how semiconductors function from the fundamental physics to the devices we use daily We cater to a general audience with a focus on readers in the United States We begin with the basics of quantum mechanics and solid state physics before diving into how these principles apply to semiconductors like silicon and gallium arsenide We explain crucial concepts such as band theory the flow of electricity through semiconductors and their use in devices like transistors and solar cells Additionally we discuss the manufacturing processes of semiconductors and highlight the advancements scientists are making in developing new and improved semiconductors Physics of Semiconductors Core Principles is an excellent resource for anyone eager to understand the intricacies of this essential technology INTELLIGENT FRONTIERS Innovations in Semiconductors,

Telecom, And AI Systems Goutham Kumar Sheelam, Botlagunta Preethish Nandan, Semiconductors and Superconductors Ron Legarski, Yash Patel, Zoltan Csernus, 2024-09-22 Semiconductors and Superconductors From Invention to Innovation is a

comprehensive exploration of the fundamental technologies that power modern electronics energy systems and computing Written by Ron Legarski a leading expert in telecommunications and technology solutions this book delves into the discovery evolution and future applications of semiconductors and superconductors two cornerstones of modern science and engineering The book is designed for a wide audience from professionals in the tech industry and academic researchers to students and general readers interested in understanding the science and technology that drive today s digital world Semiconductors are the building blocks of every microchip transistor and integrated circuit essential components in everything from smartphones to solar cells Superconductors on the other hand have the potential to revolutionize fields like energy transmission quantum computing and medical imaging by enabling technologies that operate with zero electrical resistance This book covers the key milestones in the development of semiconductors and superconductors starting with the invention of the transistor and the discovery of superconductivity It also dives into the applications of these technologies in industries such as telecommunications computing energy systems and medical technology demonstrating their far reaching impact on society Key topics include The physics of semiconductors and superconductors explained in accessible language The history and evolution of transistors integrated circuits and quantum devices How superconducting materials are used in applications ranging from MRI machines to high speed trains The role of semiconductors in smartphones AI systems and energy efficient power grids Future research directions including the pursuit of room temperature superconductors and wide bandgap semiconductors like SiC and GaN The convergence of AI machine learning and nanotechnology in designing next generation semiconductor and superconductor devices The book also provides a forward looking perspective on how these technologies will shape the future particularly in fields like quantum computing artificial intelligence and renewable energy systems With chapters organized for easy navigation technical glossaries and suggested reading for further exploration Semiconductors and Superconductors From Invention to Innovation is an essential resource for anyone looking to understand the technological forces that are driving the world forward **Handbook of Semiconductors** Ram K. Gupta,2024-07-10

This book provides readers with state of the art knowledge of established and emerging semiconducting materials their processing and the fabrication of chips and microprocessors In addition to covering the fundamentals of these materials it details the basics and workings of many semiconducting devices and their role in modern electronics and explores emerging semiconductors and their importance in future devices Provides readers with latest advances in semiconductors Covers diodes transistors and other devices using semiconducting materials Covers advances and challenges in semiconductors and their technological applications Discusses fundamentals and characteristics of emerging semiconductors for chip manufacturing This book provides directions to scientists engineers and researchers in materials engineering and related disciplines to help them better understand the physics characteristics and applications of modern semiconductors

Fundamental Properties of Semiconductor Nanowires Naoki Fukata,Riccardo Rurali,2020-11-16 This book covers

virtually all aspects of semiconductor nanowires from growth to related applications in detail First it addresses nanowires growth mechanism one of the most important topics at the forefront of nanowire research The focus then shifts to surface functionalization nanowires have a high surface to volume ratio and thus are well suited to surface modification which effectively functionalizes them The book also discusses the latest advances in the study of impurity doping a crucial process in nanowires In addition considerable attention is paid to characterization techniques such as nanoscale and in situ methods which are indispensable for understanding the novel properties of nanowires Theoretical calculations are also essential to understanding nanowires characteristics particularly those that derive directly from their special nature as one dimensional nanoscale structures In closing the book considers future applications of nanowire structures in devices such as FETs and lasers

Fundamentals of III-V Semiconductor MOSFETs Serge Oktyabrsky, Peide Ye, 2010-03-16 *Fundamentals of III V Semiconductor MOSFETs* presents the fundamentals and current status of research of compound semiconductor metal oxide semiconductor field effect transistors MOSFETs that are envisioned as a future replacement of silicon in digital circuits The material covered begins with a review of specific properties of III V semiconductors and available technologies making them attractive to MOSFET technology such as band engineered heterostructures effect of strain nanoscale control during epitaxial growth Due to the lack of thermodynamically stable native oxides on III V s such as SiO₂ on Si high k oxides are the natural choice of dielectrics for III V MOSFETs The key challenge of the III V MOSFET technology is a high quality thermodynamically stable gate dielectric that passivates the interface states similar to SiO₂ on Si Several chapters give a detailed description of materials science and electronic behavior of various dielectrics and related interfaces as well as physics of fabricated devices and MOSFET fabrication technologies Topics also include recent progress and understanding of various materials systems specific issues for electrical measurement of gate stacks and FETs with low and wide bandgap channels and high interface trap density possible paths of integration of different semiconductor materials on Si platform

State-of-the-Art Program on Compound Semiconductors 50 (SOTAPOCS 50) -and- Processes at the Semiconductor Solution Interface 3 A. G. Baca, 2009-05 This issue of ECS Transactions contain the most recent developments in compound semiconductors encompassing advanced devices materials growth characterization processing device fabrication reliability and other related topics as well as the most recent developments in processes at the semiconductor solution interface including etching oxidation passivation film growth electrochemical and photoelectrochemical processes electroluminescence photoluminescence and other related topics

Compound Semiconductors Strained Layers and Devices Suresh Jain, Magnus Willander, R. Van Overstraeten, 2013-11-27 In recent years extensive work has been done on strain dislocations and mechanical properties of strained layers Although it is not possible to describe all this work in a monograph of this size *Compound Semiconductors Strained Layers and Devices* provides an overview with sufficient detail to cover all the essential aspects of recent developments in the field The book concentrates on compound semiconductors with emphasis on wideband

gap II VI and III Nitride semiconductors GeSi strained layers are discussed for comparison to clarify the underlying physics The effects of strain on band structure transport and optical properties of both the zinc blende and the wurtzite compound semiconductors are discussed as are Piezoelectric Effects and Quantum Confined Stark Effects Magnetic polarons in diluted II VI magnetic polarons are also covered Among the applications blue and green LEDs and LDs and mid IR LDs are included A whole chapter is devoted to these devices Another chapter examines transistors based on conventional III V II VI and III nitride semiconductors The subject matter is treated at a level appropriate for students and senior researchers interested in material science and in designing and modeling semiconductor devices It will also be useful to engineers and material scientists concerned with the effects of strain on the mechanical properties of crystalline layers of any material *III-V Nitride Semiconductors* Edward T. Yu, 2022-10-30 The concepts in this book will provide a comprehensive overview of the current state for a broad range of nitride semiconductor devices as well as a detailed introduction to selected materials and processing issues of general relevance for these applications This compilation is very timely given the level of interest and the current stage of research in nitride semiconductor materials and device applications This volume consists of chapters written by a number of leading researchers in nitride materials and device technology addressing Ohmic and Schottky contacts AlGaInN multiple quantum well laser diodes nitride vertical cavity emitting lasers and ultraviolet photodetectors This unique volume provides a comprehensive review and introduction to application and devices based on GaN and related compounds for newcomers to the field and stimulus to further advances for experienced researchers

Epitaxy of Semiconductors Udo W. Pohl, 2020-07-20 The extended and revised edition of this textbook provides essential information for a comprehensive upper level graduate course on the crystalline growth of semiconductor heterostructures Heteroepitaxy is the basis of today's advanced electronic and optoelectronic devices and it is considered one of the most important fields in materials research and nanotechnology The book discusses the structural and electronic properties of strained epitaxial layers the thermodynamics and kinetics of layer growth and it describes the major growth techniques metalorganic vapor phase epitaxy molecular beam epitaxy and liquid phase epitaxy It also examines in detail cubic and hexagonal semiconductors strain relaxation by misfit dislocations strain and confinement effects on electronic states surface structures and processes during nucleation and growth Requiring only minimal knowledge of solid state physics it provides natural sciences materials science and electrical engineering students and their lecturers elementary introductions to the theory and practice of epitaxial growth supported by references and over 300 detailed illustrations In this second edition many topics have been extended and treated in more detail e.g. in situ growth monitoring application of surfactants properties of dislocations and defects in organic crystals and special growth techniques like vapor liquid solid growth of nanowires and selective area epitaxy

Semiconductor Cleaning Science and Technology 14 (SCST 14) T. Hattori, P. Mertens, R. Novak, J. Ruzyllo, 2015

Integration of Functional Oxides with Semiconductors Alexander A. Demkov, Agham B.

Posadas,2014-02-20 This book describes the basic physical principles of the oxide semiconductor epitaxy and offers a view of the current state of the field It shows how this technology enables large scale integration of oxide electronic and photonic devices and describes possible hybrid semiconductor oxide systems The book incorporates both theoretical and experimental advances to explore the heteroepitaxy of tuned functional oxides and semiconductors to identify material device and characterization challenges and to present the incredible potential in the realization of multifunctional devices and monolithic integration of materials and devices Intended for a multidisciplinary audience Integration of Functional Oxides with Semiconductors describes processing techniques that enable atomic level control of stoichiometry and structure and reviews characterization techniques for films interfaces and device performance parameters Fundamental challenges involved in joining covalent and ionic systems chemical interactions at interfaces multi element materials that are sensitive to atomic level compositional and structural changes are discussed in the context of the latest literature Magnetic ferroelectric and piezoelectric materials and the coupling between them will also be discussed GaN SiC Si GaAs and Ge semiconductors are covered within the context of optimizing next generation device performance for monolithic device processing

Semiconductors, Dielectrics, and Metals for Nanoelectronics 15: In Memory of Samares Kar D. Misra,S. De Gendt,M. Houssa,K. Kita,D. Landheer, *Ultrathin Two-Dimensional Semiconductors for Novel Electronic Applications* Mohammad Karbalaee Akbari,Serge Zhuiykov,2020-07-30 Offering perspective on both the scientific and engineering aspects of 2D semiconductors Ultrathin Two Dimensional Semiconductors for Novel Electronic Applications discusses how to successfully engineer 2D materials for practical applications It also covers several novel topics regarding 2D semiconductors which have not yet been discussed in any other publications Features Provides comprehensive information and data about wafer scale deposition of 2D semiconductors ranging from scientific discussions up to the planning of experiments and reliability testing of the fabricated samples Precisely discusses wafer scale ALD and CVD of 2D semiconductors and investigates various aspects of deposition techniques Covers the new group of 2D materials synthesized from surface oxide of liquid metals and also explains the device fabrication and post treatment of these 2D nanostructures Addresses a wide range of scientific and practical applications of 2D semiconductors and electronic and optoelectronic devices based on these nanostructures Offers novel coverage of 2D heterostructures and heterointerfaces and provides practical information about fabrication and application of these heterostructures Introduces the latest advancement in fabrication of novel memristors artificial synapses and sensorimotor devices based on 2D semiconductors This work offers practical information valuable for engineering applications that will appeal to researchers academics and scientists working with and interested in developing an array of semiconductor electronic devices **Comprehensive Semiconductor Science and Technology** ,2024-11-28

Semiconductors are at the heart of modern living Almost everything we do be it work travel communication or entertainment all depend on some feature of semiconductor technology Comprehensive Semiconductor Science and Technology Second

Edition Three Volume Set captures the breadth of this important field and presents it in a single source to the large audience who study make and use semiconductor devices Written and edited by a truly international team of experts and newly updated to capture key advancements in the field this work delivers an objective yet cohesive review of the semiconductor world The work is divided into three sections fully updated and expanded from the first edition The first section is concerned with the fundamental physics of semiconductors showing how the electronic features and the lattice dynamics change drastically when systems vary from bulk to a low dimensional structure and further to a nanometer size Throughout this section there is an emphasis on the full understanding of the underlying physics especially quantum phenomena The second section deals largely with the transformation of the conceptual framework of solid state physics into devices and systems which require the growth of high purity or doped bulk and epitaxial materials with low defect density and well controlled electrical and optical properties The third section is devoted to design fabrication and assessment of discrete and integrated semiconductor devices It will cover the entire spectrum of devices we see all around us for telecommunications computing automation displays illumination and consumer electronics Provides a comprehensive global picture of the semiconductor world Written and Edited by an international team of experts Compiles the most important semiconductor knowledge into one comprehensive resource Moves from fundamentals and theory to more advanced knowledge such as applications allowing readers to gain a deeper understanding of the field

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