

The JTAGICE3 Debugger



The Atmel JTAGICE3 is a powerful development tool for debugging and programming ARM® Cortex®-M based Atmel® SAM and Atmel AVR® microcontrollers with On-Chip Debug capability.

- Programming and on-chip debugging of all Atmel AVR 32-bit microcontrollers on both JTAG and I2C interfaces.
- Programming and on-chip debugging of all Atmel AVR XMEGA family devices on both JTAG and PDI 2-wire interfaces.
- Programming (JTAG and SPI) and debugging of all Atmel AVR 8-bit microcontrollers with OCD support on both JTAG or debugWIRE interfaces.
- Programming and debugging of all Atmel SAM ARM® Cortex®-M based microcontrollers on both SWD and JTAG interfaces. (Firmware version 3.0 and later)

Atmel Jtagice3 Manual

PT Brinkman



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