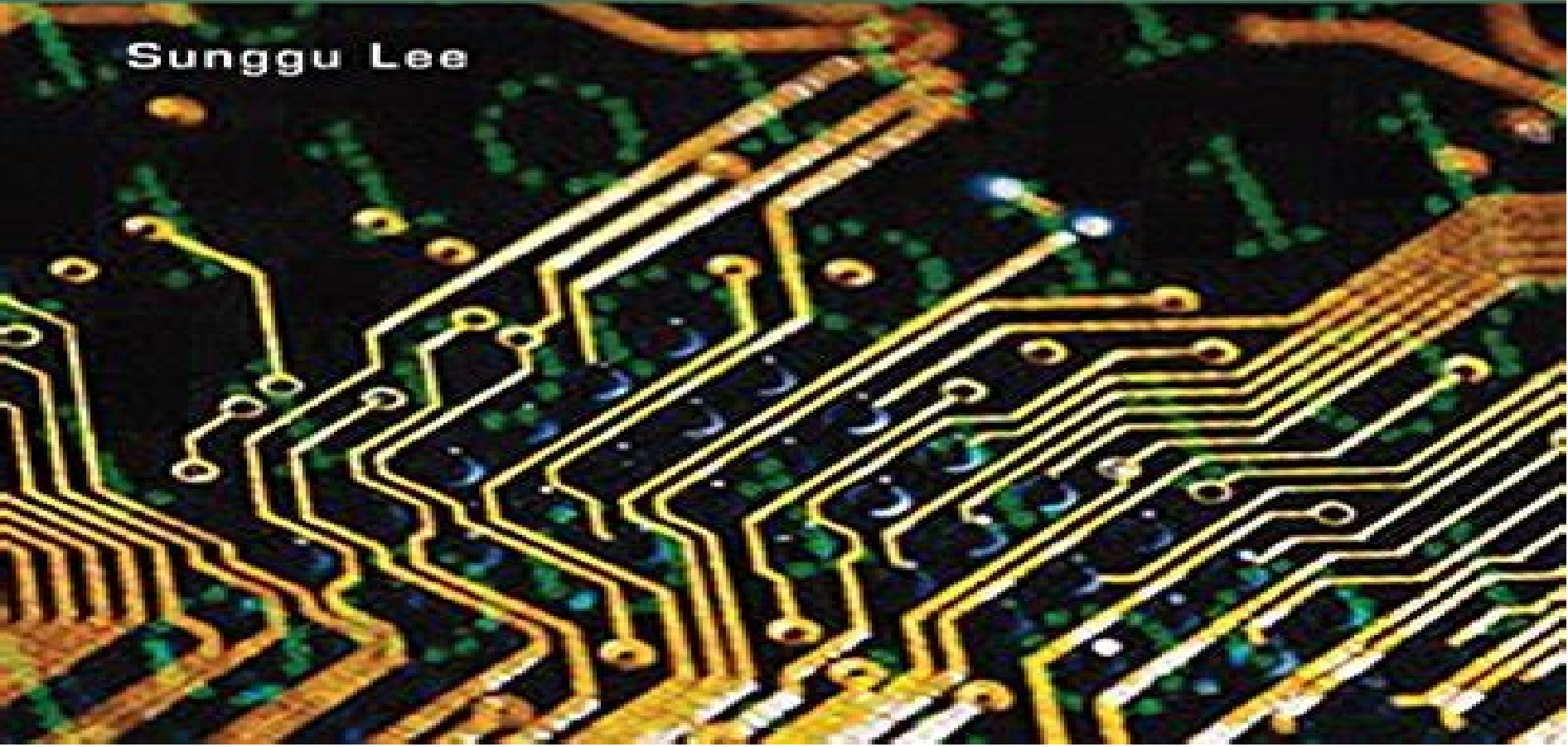


# ADVANCED DIGITAL LOGIC DESIGN

using Verilog, State Machines  
and Synthesis for FPGAs

Sunggu Lee



# Advanced Digital Logic Design Using Vhdl State Machines And Synthesis For Fpgas

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