

ARM Assembly Language Guide

ARM is an example of a Reduced Instruction Set Computer (RISC) which was designed for easy instruction pipelining. ARM has a "Load/Store" architecture since all instructions (other than the load and store instructions) must use register operands. ARM has 16 32-bit "general purpose" registers (r0, r1, r2, ..., r15), but some of these have special uses (see ARM Register Conventions table on page 4).

Common ARM Instructions (and psuedo-instructions)		
Type of Instruction	ARM Assembly Language	Register Transfer Language Description
Memory Access (Load and Store)	LDR r4, Mem	$[r4] \leftarrow [Mem]$; Mem is a global variable label
	STR r4, Mem	$[Mem] \leftarrow [r4]$
	LDR r4, [r3]	$[r4] \leftarrow [[r3]]$; <i>register indirect</i>
	STR r4, [r3, #4]	$[[r3] + 4] \leftarrow [r4]$; <i>register indirect with offset</i>
Move	MOV r4, r2	$[r4] \leftarrow [r2]$
	MOV r4, #10	$[r4] \leftarrow 10$; 8-bit literal, but can be shifted
Load Address	ADR r4, Mem	$[r4] \leftarrow$ load address of label Mem
Arithmetic Instruction (reg. operands only, except last operand can be an 8-bit integer)	ADD r4, r2, r3	$[r4] \leftarrow [r2] + [r3]$
	MUL r4, r2, r3	$[r4] \leftarrow [r2] * [r3]$ (32-bit product)
	SUB r4, r2, r3	$[r4] \leftarrow [r2] - [r3]$
Compare (sets condition codes)	CMP r4, r2	Sets condition codes by $r4 - r2$
Conditional Branch	BGT LABEL (BGE, BLT, BLE, BEQ, BNE)	Branch to LABEL if $r4 > r2$ if it follow above CMP
Unconditional Branch	B LABEL	Always Branch to LABEL

A simple ARM assembly language program to sum the elements in an array A is given below:

```

; ARM Example that sums an array via the algorithm:
; SUM = 0
; for I = 0 to LENGTH - 1 do
;     SUM = SUM + ARRAY[I]
; end for
AREA SUMARRAY, CODE, READONLY
ENTRY
; Always needed to indicate where to start pgm
LDR r2, LENGTH
SUB r2, r2, #1
; r2 contains (LENGTH-1)
MOV r6, #0
; r6 sum set to 0
FOR_INIT MOV r1, #0
; r1 index I set to 0
FOR_CMP  ADR r3, ARRAY
; start r3 with address of A[0]
CMP r1, r2
; compare I and (LENGTH-1)
BGT END_FOR
; drop out of loop if I > (LENGTH-1)
LDR r4, [r3], #4
; load r4 with A[I] then walk r3 down ARRAY
ADD r6, r6, r4
; update sum with A[I]
ADD r1, r1, #1
; increment I
B FOR_CMP
; loop back to for-loop check
END_FOR
STR r6, SUM
; store result in SUM
STOP
B STOP

AREA SUMARRAY, DATA, READWRITE
ALIGN
SUM DCD 0xFFFFFFFF
ARRAY DCD 5, 10, 15, 20, 30, 40, 50
LENGTH DCD 7

END
; Needed to stop assembly

```

Assembly Language Manual

Rosina Ehmann



Assembly Language Manual:

Unveiling the Power of Verbal Artistry: An Psychological Sojourn through **Assembly Language Manual**

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Table of Contents Assembly Language Manual

1. Understanding the eBook Assembly Language Manual
 - The Rise of Digital Reading Assembly Language Manual
 - Advantages of eBooks Over Traditional Books
2. Identifying Assembly Language Manual
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Assembly Language Manual
 - User-Friendly Interface
4. Exploring eBook Recommendations from Assembly Language Manual
 - Personalized Recommendations

- Assembly Language Manual User Reviews and Ratings
- Assembly Language Manual and Bestseller Lists
- 5. Accessing Assembly Language Manual Free and Paid eBooks
 - Assembly Language Manual Public Domain eBooks
 - Assembly Language Manual eBook Subscription Services
 - Assembly Language Manual Budget-Friendly Options
- 6. Navigating Assembly Language Manual eBook Formats
 - ePub, PDF, MOBI, and More
 - Assembly Language Manual Compatibility with Devices
 - Assembly Language Manual Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Assembly Language Manual
 - Highlighting and Note-Taking Assembly Language Manual
 - Interactive Elements Assembly Language Manual
- 8. Staying Engaged with Assembly Language Manual
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Assembly Language Manual
- 9. Balancing eBooks and Physical Books Assembly Language Manual
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Assembly Language Manual
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Assembly Language Manual
 - Setting Reading Goals Assembly Language Manual
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Assembly Language Manual
 - Fact-Checking eBook Content of Assembly Language Manual

- Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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