# Microcontroller Solutions

36.CLR C ;CY=0 CPL C; CY

37.G

- a. CY=1
- b. CY=0
- c. CY=0

38.ORG 0000H

MOV A,#55H

ADD A,#55H

ADD A,#55H

ADD A,#55H

ADD A,#55H

END

39.RSO and RS1.

40.On Startup Stack Location is 07H.

42.24 Bytes.

43.Register Bank 0

44.Register Bank 0 from 00H to 07H.

Register Bank 1 from 08H to 0FH.

Register Bank 2 from 10H to 17H.

Register Bank 3 from 18H to 1FH.

45.(a) 04H (b) 00H (c) 07H (d) 05H

# 8051 Microcontroller Mazidi Solution Manual 134181

Muhammad Ali Mazidi, Janice Gillispie Mazidi, Rolin D. Mckinlay

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