

START:	IN F2H	2000	D8F2
	CMA	2002	2F
	ORA A	2003	B7
	JZ START	2004	CA0020

10. Logical steps to add two Hex numbers:

Load A2H in one register.
 Load 18H in second register.
 Copy A2H in the accumulator.
 Add the contents of the second register to the contents of the accumulator.
 End of the program.

11. MVI B, A2H
 MVI C, 18H
 MOV A, B
 ADD C
 HLT

12. Register contents:

Initial: B=28H, A=97H

After the execution : A=28H, B=28H, C=28H

13. In Q. 6, if the code 07H (port address) is omitted, the processor assumes the opcode of the next instruction 76H (HLT) as the address of the output port, outputs the contents of the accumulator to the address 76H, and continues to the next code. After the next code, results are indeterminate.
14. In Q. 8, if the byte 0FH is omitted, the processor assumes the opcode 32H of the next instruction (STA) as the second byte of the ANI instruction. The processor is a sequential machine; it assumes the next code 20H (the low-order address of 2070H) as the opcode of the next instruction and continues.

CHAPTER 6

Section 6.1: Data Transfer (Copy) Operations

- 1.
- | | A | B | C | D | E | F | CY |
|----------|----|----|----|---|----|----|----|
| MVI A,00 | 00 | | | | NA | NA | NA |
| MVI B,FB | 00 | FB | | | | | |
| MOV C,A | 00 | FB | 00 | | | | |

8085 Gaonkar Solution Manual

Marco Cascella



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Engineering Education ,1984 **Computer Books and Serials in Print** ,1985 **Books in Print Supplement** ,2002

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