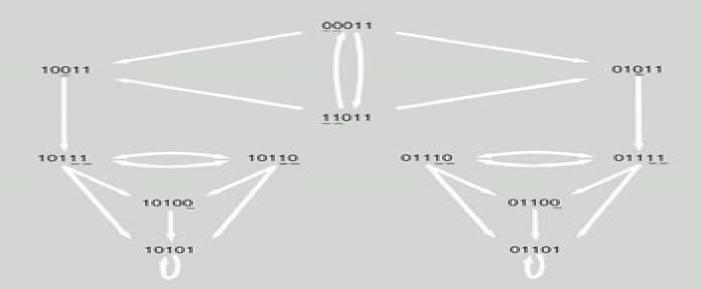
# ASYNCHRONOUS CIRCUITS

Janusz A. Brzozowski Carl-Johan H. Seger





# <u>Asynchronous Circuits Monographs In Computer</u> <u>Science</u>

**Luca De Alfaro** 

## **Asynchronous Circuits Monographs In Computer Science:**

**Asynchronous Circuits** Janusz A. Brzozowski, Carl-Johan H. Seger, 1995-02-24 In recent years there has been a great surge of interest in asynchronous circuits largely through the development of new asynchronous design methodologies This book provides a comprehensive theory of asynchronous circuits including modelling analysis simulation specification verification and an introduction to their design Principles of Asynchronous Circuit Design Jens Sparsø, Steve Furber.2013-04-17 Principles of Asynchronous Circuit Design A Systems Perspective addresses the need for an introductory text on asynchronous circuit design Part I is an 8 chapter tutorial which addresses the most important issues for the beginner including how to think about asynchronous systems Part II is a 4 chapter introduction to Balsa a freely available synthesis system for asynchronous circuits which will enable the reader to get hands on experience of designing high level asynchronous systems Part III offers a number of examples of state of the art asynchronous systems to illustrate what can be built using asynchronous techniques The examples range from a complete commercial smart card chip to complex microprocessors The objective in writing this book has been to enable industrial designers with a background in conventional clocked design to be able to understand asynchronous design sufficiently to assess what it has to offer and whether it might be advantageous in their next design task Asynchronous Circuits Janusz A. Brzozowski, Carl-Johan H. Seger, 2012-12-06 Although asynchronous circuits date back to the early 1950s most of the digital circuits in use today are synchronous because traditionally asynchronous circuits have been viewed as difficult to understand and design In recent years however there has been a great surge of interest in asynchronous circuits largely through the development of new asynchronous design methodologies This book provides a comprehensive theory of asynchronous circuits including modelling analysis simulation specification verification and an introduction to their design It is based on courses given to graduate students and will be suitable for computer scientists and engineers involved in the research and development of asynchronous designs

Role Of Theory In Computer Science, The: Essays Dedicated To Janusz Brzozowski Stavros Konstantinidis, Nelma Moreira, Rogerio Reis, Jeffrey Shallit, 2017-04-25 This volume brings together the work of several prominent researchers who have collaborated with Janusz Brzozowski or worked in topics he developed in the areas of regular languages syntactic semigroups of formal languages the dot depth hierarchy and formal modeling of circuit testing and software specification using automata theory Asynchronous Operators of Sequential Logic: Venjunction & Sequention Vadim Vasyukevich, 2011-06-18 This book is dedicated to new mathematical instruments assigned for logical modeling of the memory of digital devices The case in point is logic dynamical operation named venjunction and venjunctive function as well as sequention and sequentional function Venjunction and sequention operate within the framework of sequential logic In a form of the corresponding equations they organically fit analytical expressions of Boolean algebra Thus a sort of symbiosis is formed using elements of asynchronous sequential logic on the one hand and combinational logic on the other hand So

asynchronous logic is represented in the form of enhanced Boolean logic The book contains initial concepts fundamental definitions statements principles and rules needed for theoretical justification of the mathematical apparatus and its validity for asynchronous logic Asynchronous operators named venjunctor and sequentor are designed for practical implementation These basic elements are assigned for realizing of memory functions in sequential circuits Present research work is the final stage of generalization and systematization of all those ideas and investigations author's interest to which alternately flashed up and faded over many years and for various reasons until formed critical mass and all findings were arranged definitively as a mathematical basis of a theory appropriately associated under a common theme asynchronous sequential logic essentially classified as switching logic which falls into category of algebraic logics Model-Based Testing for Embedded Systems Justyna Zander, Ina Schieferdecker, Pieter J. Mosterman, 2017-12-19 What the experts have to say about Model Based Testing for Embedded Systems This book is exactly what is needed at the exact right time in this fast growing area From its beginnings over 10 years ago of deriving tests from UML statecharts model based testing has matured into a topic with both breadth and depth Testing embedded systems is a natural application of MBT and this book hits the nail exactly on the head Numerous topics are presented clearly thoroughly and concisely in this cutting edge book The authors are world class leading experts in this area and teach us well used and validated techniques along with new ideas for solving hard problems It is rare that a book can take recent research advances and present them in a form ready for practical use but this book accomplishes that and more I am anxious to recommend this in my consulting and to teach a new class to my students Dr Jeff Offutt professor of software engineering George Mason University Fairfax Virginia USA This handbook is the best resource I am aware of on the automated testing of embedded systems It is thorough comprehensive and authoritative It covers all important technical and scientific aspects but also provides highly interesting insights into the state of practice of model based testing for embedded systems Dr Lionel C Briand IEEE Fellow Simula Research Laboratory Lysaker Norway and professor at the University of Oslo Norway As model based testing is entering the mainstream such a comprehensive and intelligible book is a must read for anyone looking for more information about improved testing methods for embedded systems Illustrated with numerous aspects of these techniques from many contributors it gives a clear picture of what the state of the art is today Dr Bruno Legeard CTO of Smartesting professor of Software Engineering at the University of Franche Comt Besan on France and co author of Practical Model Based Testing A Discipline of Multiprogramming Jayadev Misra, 2012-09-07 In this book a programming model is developed that addresses the fundamental issues of large scale programming unifying several concepts from database theory object oriented programming and designs of reactive systems The model and the associated theory have been christened Seuss The major goal of Seuss is to simplify multiprogramming To this end we separate the concern of concurrent implementation from the core program design problem A program execution is understood as a single thread of control sequential executions of actions that are chosen according to

some scheduling policy yet program implementation permits concurrent executions of multiple threads As a consequence it is possible to reason about the properties of a program from its single execution thread whereas an implementation may exploit the inherent concurrency for efficient execution Foundations of Software Science and Computational Structures Luca De Alfaro, 2009-03-09 This book constitutes the refereed proceedings of the 12th International Conference on Foundations of Software Science and Computational Structures FOSSACS 2009 held in York UK in March 2009 as part of ETAPS 2009 the European Joint Conferences on Theory and Practice of Software The 30 revised full papers presented together with two invited talks were carefully reviewed and selected from 102 full paper submissions. The topics addressed are semantics logics and automata algebras automata theory processes and models security probabilistic and quantitative models synthesis and program analysis and semantics Foundations of Software Technology and Theoretical Computer Science Rudrapatna K. Shyamasundar, 1993-11-23 For more than a decade Foundations of Software Technology and Theoretical Computer Science Conferences have been providing an annual forum for the presentation of new research results in India and abroad This year 119 papers from 20 countries were submitted Each paper was reviewed by at least three reviewers and 33 papers were selected for presentation and included in this volume grouped into parts on type theory parallel algorithms term rewriting logic and constraint logic programming computational geometry and complexity software technology concurrency distributed algorithms and algorithms and learning theory Also included in the volume are the five invited papers presented at theconference Theorem Provers in Circuit Design Ramayya Kumar, Thomas Kropf, 1995-03-06 This two volume set contains papers presented at the International Conference on Computational Engineering Science ICES 95 held in Mauna Lani Hawaii from 30 July to 3 August 1995 The contributions capture the state of the science in computational modeling and simulation in a variety of engineering disciplines civil mechanical aerospace materials and electronics engineering

Lectures on Petri Nets II: Applications Wolfgang Reisig, Grzegorz Rozenberg, 1998-11-04 The two volume set originates from the Advanced Course on Petri Nets held in Dagstuhl Germany in September 1996 beyond the lectures given there additional chapters have been commissioned to give a well balanced presentation of the state of the art in the area Together with its companion volume Lectures on Petri Nets I Basic Models this book is the actual reference for the area and addresses professionals students lecturers and researchers who are interested in systems design and would like to learn to use Petri nets familiar with subareas of the theory or its applications and wish to view the whole area interested in learning about recent results presented within a unified framework planning to apply Petri nets in practical situations interested in the relationship of Petri nets to other models of concurrent systems

On a Method of Multiprogramming W.H.J.
Feijen,A.J.M. van Gasteren,2013-06-29 Among all the interests in parallelism there is an essential and fundamental one that has remained largely unexplored namely the question of how to design parallel programs from their specification And that is what this book is about It proposes a method for the formal development of parallel programs multiprograms as we have

preferred to call them and it does so with a minimum of formal gear viz with the predicate calculus and with the meanwhile well established theory of Owicki and Gries The fact that one can get away with just this theory will probably not convey anything to the uninitiated but it may all the more come as a surprise to those who were exposed earlier to correctness of multiprograms Contrary to common belief the Owicki Gries theory can indeed be effectively put to work for the formal development of multiprograms regardless of whether these algorithms are distributed or not That is what we intend to exemplify with this book Set Theory for Computing Domenico Cantone, Eugenio Omodeo, Alberto Policriti, 2013-06-29 Set Theory for Computing offers an up to date and comprehensive account of set oriented symbolic manipulation and automated reasoning methods Mastering today s variety of systems with crisp formal tools is a prerequisite for a high degree of control over sets and aggregates The many algorithmic methods and deductive techniques in this book offer readers a clear view of the use of set theoretic notions in such critical areas as specification of problems data types and solution methods algorithmic program verification and automated deduction The rigorous and largely self contained style of presentation addresses readers wanting to complement their set intuition with the ability to exploit it in specification and verification and master it by symbolic logically based techniques and methods This book will be of interest to graduates and researchers in theoretical computer science and computational logic and automated reasoning **Formal Methods in Computer-Aided Design** Mark D. Aagaard, John W. O'Leary, 2003-06-30 This volume contains the proceedings of the Fourth Biennial Conference on F mal Methods in Computer Aided Design FMCAD The conference is devoted to the use of mathematical methods for the analysis of digital hardware c cuits and systems The workreported in this bookdescribes the use of formal mathematics and associated tools to design and verify digital hardware systems Functional veri cation has become one of the principal costs in a modern computer design e ort FMCAD provides a venue for academic and industrial researchers and practitioners to share their ideas and experiences of using screte mathematical modeling and veri cation Over the past 20 years this area has grown from just a few academic researchers to a vibrant worldwide com nity of people from both academia and industry This volume includes 23 papers selected from the 47 submitted papers each of which was reviewed by at least three program committee members The history of FMCAD dates backto 1984 when the earliest meetings on this topic occurred as part of IFIP WG10 2 Programming Methodology Annabelle Mclver, Carroll Morgan, 2012-12-06 The second half of the twentieth century saw an astonishing increase in computing power today computers are unbelievably faster than they used to be they have more memory they can communicate routinely with remote machines all over the world and they can fit on a desktop But despite this remarkable progress the voracity of modem applications and user expectations still pushes technology right to the limit As hardware engineers build ever more powerful machines so too must software become more sophisticated to keep up Medium to large scale programming projects need teams of people to pull everything together in an acceptable timescale The question of how pro gram mers understand their own tasks and how they fit together

with those of their colleagues to achieve the overall goal is a major concern Without that under standing it would be practically impossible to realise the commercial potential of our present day computing hardware That programming has been able to keep pace with the formidable advances in hardware is due to the similarly formidable advances in the principles for design construction and organisation of programs The efficacy of these methods and principles speaks for itself computer technology is all pervasive but even more telling is that they are beginning to feed back and influence hardware design as well The study of such methods is called programming methodology whose topics range over system and domain modelling concurrency object orientation program specification and validation That is the theme of this collection Synthesis and Verification Soha Hassoun, Tsutomu Sasao, 2012-12-06 Research and development of logic synthesis and verification have matured considerably over the past two decades Many commercial products are available and they have been critical in harnessing advances in fabrication technology to produce today s plethora of electronic components While this maturity is assuring the advances in fabrication continue to seemingly present unwieldy challenges Logic Synthesis and Verification provides a state of the art view of logic synthesis and verification It consists of fifteen chapters each focusing on a distinct aspect Each chapter presents key developments outlines future challenges and lists essential references Two unique features of this book are technical strength and comprehensiveness. The book chapters are written by twenty eight recognized leaders in the field and reviewed by equally qualified experts The topics collectively span the field Logic Synthesis and Verification fills a current gap in the existing CAD literature Each chapter contains essential information to study a topic at a great depth and to understand further developments in the field The book is intended for seniors graduate students researchers and developers of related Computer Aided Design CAD tools From the foreword The commercial success of logic synthesis and verification is due in large part to the ideas of many of the authors of this book Their innovative work contributed to design automation tools that permanently changed the course of electronic design by Aart J de Geus Chairman and CEO Synopsys Inc Formal Description Techniques and Protocol Specification, Testing and Verification Stan Budkowski, Ana Cavalli, Elie Najm, 2013-04-17 Formal Description Techniques and Protocol Specification Testing and Verification addresses formal description techniques FDTs applicable to distributed systems and communication protocols It aims to present the state of the art in theory application tools and industrialization of FDTs Among the important features presented are FDT based system and protocol engineering FDT application to distributed systems Protocol engineering Practical experience and case studies Formal Description Techniques and Protocol Specification Testing and Verification comprises the proceedings of the Joint International Conference on Formal Description Techniques for Distributed Systems and Communication Protocols and Protocol Specification Testing and Verification sponsored by the International Federation for Information Processing held in November 1998 Paris France Formal Description Techniques and Protocol Specification Testing and Verification is suitable as a secondary text for a graduate level course on Distributed Systems or

Communications and as a reference for researchers and practitioners in industry **Formal Modeling and Analysis of Timed Systems** Krishnendu Chatterjee, Thomas A. Henzinger, 2010-08-30 This book constitutes the proceedings of the 8th International Conference on Formal Modeling and Analysis of Timed Systems FORMATS 2010 held in Klosterneuburg Austria in September 2010 The 14 papers presented were carefully reviewed and selected from 31 submissions In addition the volume contains 3 invited talks and 2 invited tutorials The aim of FORMATS is to promote the study of fundamental and practical aspects of timed systems and to bring together researchers from different disciplines that share an interest in the modeling and analysis of timed systems Typical topics include foundations and semantics methods and tools and applications

Active Rules in Database Systems Norman W. Paton, 2012-09-07 Active rules provide a new and important method for designing databases and the subject is seeing an increasing amount of attention from commercial database companies This book provides a timely survey of the field from the point of view of some of the subject s most active researchers The book is divided into several parts organized by theme the first Fundamentals covers the underlying methodology reagrding active rules next comes a collection of chapters which cover formal specification rule analysis performance analysis and support tools the third part is devoted to a number of chapters covering the implementation of active rules in a number of commercial systems Finally come sections on applications and future directions that research may take All researchers in databases will find this provides a valuable overview of this topic Graph Transformations Hartmut Ehrig, 2008-08-28 This book constitutes the refereed proceedings of the 4th International Conference on Graph Transformations ICGT 2008 held in Leicester UK in September 2008 The 27 revised full papers presented together with 5 tutorial and workshop papers and 3 invited lectures were carefully selected from 57 submissions All current aspects in graph drawing are addressed including hypergraphs and termgraph rewriting applications of graph transformation execution of graph transformations compositional systems validation and verification graph languages and special transformation concepts as well as patterns and model transformations. In addition the volume contains 17 short papers of the ICGT 2008 Doctoral Symposium

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