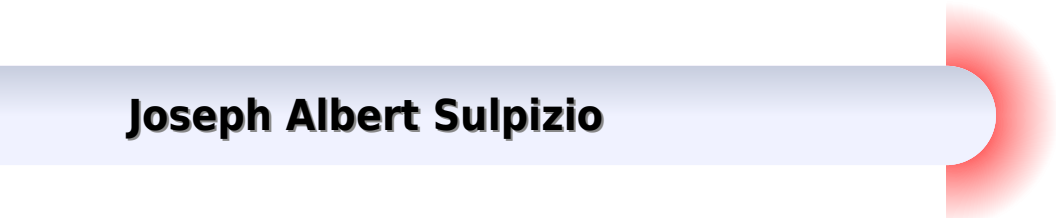




Agilent 81110a User Manual

Joseph Albert Sulpizio



Agilent 81110a User Manual:

, *Quantum Transport in One-dimensional Nanostructures* Joseph Albert Sulpizio, 2011 One dimensional 1D electronic nanostructures comprise a class of systems that boast tremendous promise for both technological innovation as well as fundamental scientific discovery To fully harness their potential it is crucial to understand transport through 1D systems at the most fundamental quantum level In this thesis we describe our investigations down three avenues of quantum transport in 1D 1 ballistic transport in quantum wires 2 quantum capacitance measurements of nanostructures and 3 tunneling measurements in carbon nanotubes First we discuss measurements and modeling of hole transport in ballistic quantum wires fabricated by GaAs AlGaAs cleaved edge overgrowth where we find strong g factor anisotropy which we associate with spin orbit coupling and evidence for the importance of charge interactions indicated by the observation of 0 7 structure Additionally we present the first experimental observation of a predicted spin orbit gap in the 1D density of states where counter propagating spins constituting a spin current are accompanied by a clear signal in the conductance Next we present the development of a highly sensitive integrated capacitance bridge for quantum capacitance measurements to be used as a novel probe of 1D systems We demonstrate the utility of our bridge by measuring the capacitance of top gated graphene devices where we cleanly resolve the density of states and also present preliminary measurements of carbon nanotube devices where we ultimately aim to extract their mobility Finally we discuss a set of transport measurements in carbon nanotubes designed to probe interactions between fermions in 1D in which top gates are used to introduce tunable tunnel barriers

Reconfigurable Field Programmable Gate Arrays for Mission-Critical Applications Niccolò

Battezzati, Luca Sterpone, Massimo Violante, 2010-11-09 Embedded systems applications that are either mission or safety critical usually entail low to mid production volumes require the rapid development of specific tasks which are typically computing intensive and are cost bounded The adoption of re configurable FPGAs in such application domains is constrained to the availability of suitable techniques to guarantee the dependability requirements entailed by critical applications This book describes the challenges faced by designers when implementing a mission or safety critical application using re configurable FPGAs and it details various techniques to overcome these challenges In addition to an overview of the key concepts of re configurable FPGAs it provides a theoretical description of the failure modes that can cause incorrect operation of re configurable FPGA based electronic systems It also outlines analysis techniques that can be used to forecast such failures and covers the theory behind solutions to mitigate fault effects This book also reviews current technologies available for building re configurable FPGAs specifically SRAM based technology and Flash based technology For each technology introduced theoretical concepts presented are applied to real cases Design techniques and tools are presented to develop critical applications using commercial off the shelf devices such as Xilinx Virtex FPGAs and Actel ProASIC FPGAs Alternative techniques based on radiation hardened FPGAs such as Xilinx SIRF and Atmel ATF280 are also presented This

publication is an invaluable reference for anyone interested in understanding the technologies of reconfigurable FPGAs as well as designers developing critical applications based on these technologies

Integration of Resistive Switching Devices in Crossbar Structures Christian Nauenheim, 2010

Cognitive Applications in Resistive Memories Lutz Stephan Nielen, 2020-08-27

The upcoming trends in information technology follow irresistibly the predicted changes of the Internet of Things IoT That requires enhanced performance of mobile electronics thus memory techniques face a growing number of challenges Redox based resistive switches ReRAM are highly attractive devices for implementation of ultimately scaled energy efficient memories In this thesis associative memories and sorting networks based on resistive switches are investigated These cognitive functions highlight the potential of brain inspired computing in memory and are considered as a key enabler for beyond von Neumann computer architectures

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